

SK1 (anode monitor out)

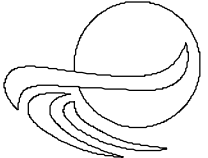
Centre	Signal
Screen	0V reference

SK2 (LVDS signal out)

Pin 1	LVDS + (red dot)
Pin 2	LVDS -

PL1 (RS485 and power in)

Pin 1	+9V power
Pin 2	+9V power
Pin 3	0V power
Pin 4	+D RS485
Pin 5	0V ref for RS485
Pin 6	+9V power
Pin 7	0V power
Pin 8	0V power
Pin 9	-D RS485

 <h1>Sens-Tech</h1> <p>SENSOR TECHNOLOGIES</p> <p>Bury Street, Ruislip, HA4 7TA, UK</p>	<p>Limits on all dimensions unless otherwise stated</p> <p>Linear dimensions - $\pm 0.1\text{mm}$</p> <p>Angular dimensions - $\pm 0.1^\circ$</p> <p>Sheet size A2 F/DO09 Iss 1</p>	<table border="1"> <tr><th>Issue</th><th>Date</th><th>C/Note No.</th><th>Sign</th><th>Material</th></tr> <tr><td>1</td><td>17.05.07</td><td></td><td></td><td></td></tr> <tr><td>2</td><td>31.05.07</td><td>S00006</td><td>JL</td><td></td></tr> </table>	Issue	Date	C/Note No.	Sign	Material	1	17.05.07				2	31.05.07	S00006	JL		<p>Drawn by M. Stoodley</p> <p>Date 17 May 2007</p> <p>Scale 1:2</p> <p>Checked M. Stoodley</p> <p>Approved J. Lewandowski</p>	<p>Project DM0066</p> <p>Title Detectors and base plate assembly</p> <p>Drawing Number 26010 B 006 Sheet 1 of 1</p>
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